

DRD560G90

Rectifier Diode

Replaces DS6066-5 June 2018 (LN35796)

FEATURES

- Double Side Cooling
- High Surge Capability

KEY PARAMETERS

V_{RRM}	9000V
I _{F(AV)}	557A
I _{FSM}	7650A

VOLTAGE RATINGS

Part and Ordering Number	Repetitive Peak Voltages V _{RRM} V	Conditions
DRD560G90 DRD560G85 DRD560G80	9000 8500 8000	$V_{RSM} = V_{RRM} + 100V$

ORDERING INFORMATION

When ordering, select the required part number shown in the Voltage Ratings selection table.

For example:

DRD560G85 for a 8500V device

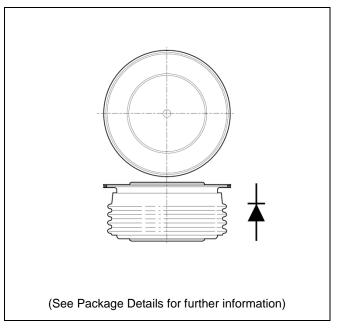


Fig. 1 Package outline

CURRENT RATINGS

$T_{\text{case}} = 75^{\circ}\text{C}$ unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units		
Double Si	Double Side Cooled					
I _{F(AV)}	Mean forward current	Half wave resistive load	684	Α		
I _{F(RMS)}	RMS value	-	1074	А		
I _F	Continuous (direct) on-state current	-	1019	Α		
Single Side Cooled (Anode side)						
I _{F(AV)}	Mean forward current	Half wave resistive load	461	Α		
I _{F(RMS)}	RMS value	-	724	Α		
l _F	Continuous (direct) on-state current	-	654	А		

T_{case} = 100°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units			
Double Si	Double Side Cooled						
I _{F(AV)}	Mean forward current	Half wave resistive load	557	А			
I _{F(RMS)}	RMS value	-	876	Α			
I _F	Continuous (direct) on-state current	-	819	Α			
Single Sic	le Cooled (Anode side)						
$I_{F(AV)}$	Mean forward current	Half wave resistive load	372	Α			
$I_{F(RMS)}$	RMS value	-	584	Α			
I _F	Continuous (direct) on-state current	-	517	А			

SURGE RATINGS

Symbol	Parameter	Test Conditions		Units
I _{FSM}	Surge (non-repetitive) on-state current	10ms half sine, T _{case} = 150°C	7.65	kA
l ² t	I ² t for fusing	$V_R = 0$	0.29	MA ² s

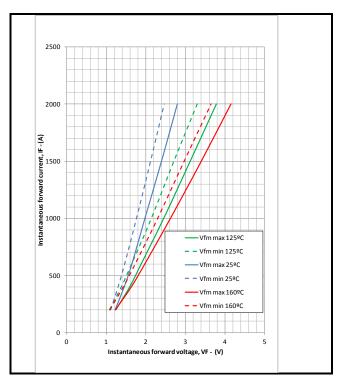
THERMAL AND MECHANICAL RATINGS

Symbol	Parameter	Test Conditions		Min.	Max.	Units
R _{th(j-c)}	Thermal resistance – junction to case	Double side cooled	DC	-	0.032	°C/W
		Single side cooled	Anode DC	-	0.064	°C/W
			Cathode DC	-	0.064	°C/W
R _{th(c-h)}	Thermal resistance – case to heatsink	Clamping force 43kN	Double side	-	.008	°C/W
		(with mounting compound)	Single side	-	.016	°C/W
T_{vj}	Virtual junction temperature			-	160	°C
T _{stg}	Storage temperature range			-55	175	°C
Fm	Clamping force			11	13	kN

CHARACTERISTICS

Symbol	Parameter	Test Conditions	Min.	Max.	Units
V _{FM}	Forward voltage	At 1200A peak, T _{case} = 160°C	-	2.95	V
I _{RM}	Peak reverse current	At V _{RRM} , T _{case} = 160°C	-	100	mA
Qs	Total stored charge	I _F = 2000A, dI _{RR} /dt =5A/μs	2840	4300	μC
Irr	Peak reverse recovery current	$T_{case} = 160$ °C, $V_R = 100$ V	140	170	Α
V _{TO}	Threshold voltage	At T _{vj} = 160°C	-	1.0	V
r _T	Slope resistance	At T _{vj} = 160°C	-	1.575	mΩ

CURVES



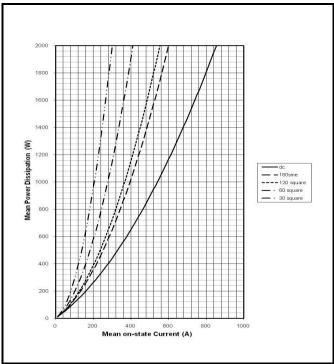


Fig.2 Maximum & minimum on-state characteristics

Fig.3 Dissipation curves

 V_{TM} EQUATION

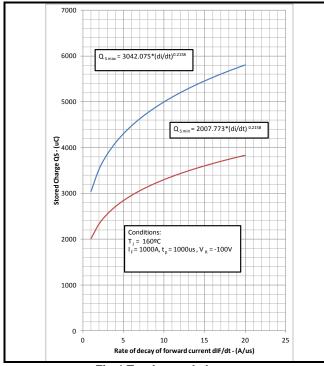
 $V_{TM} = A + BIn (I_T) + C.I_T + D.\sqrt{I_T}$

Where A = -0.675901B = 0.3995

C = 0.001796

D = -0.040301

these values are valid for $T_j = 160$ °C for $I_F 200$ A to 2000A



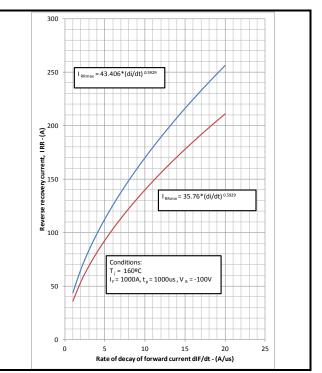
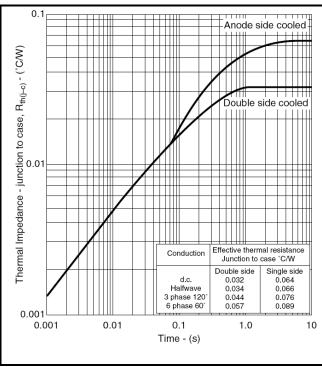
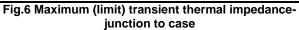


Fig.4 Total stored charge







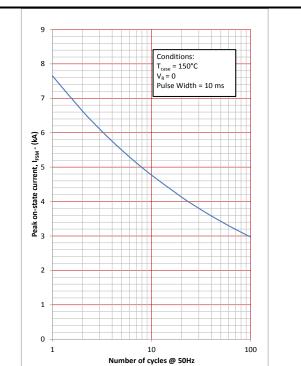


Fig. 7 Surge forward current vs number of half cycles at 50Hz

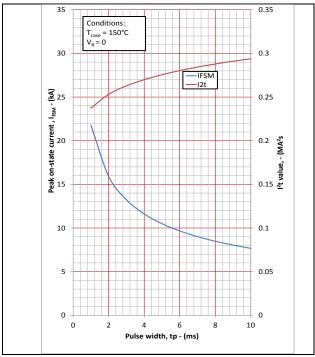
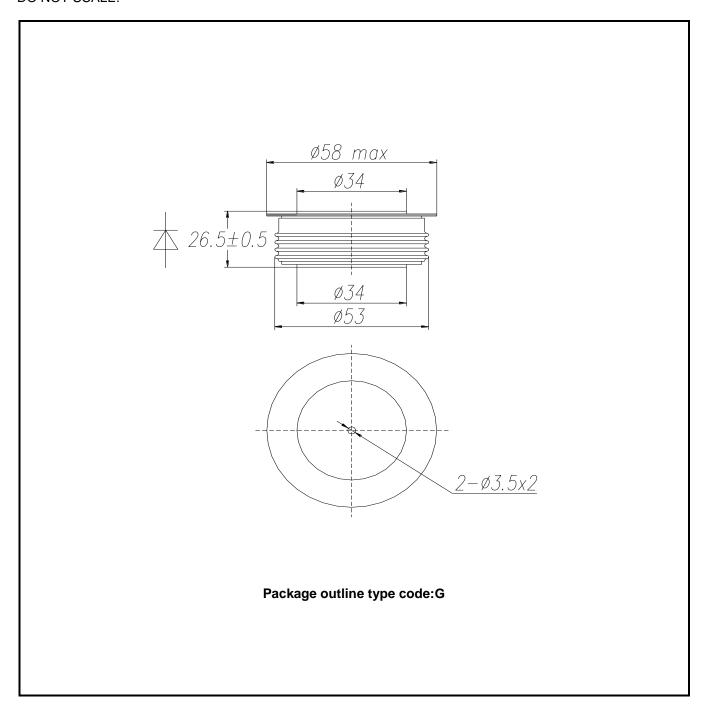


Fig. 8 Surge (non-repetitive) forward current vs pulse width

PACKAGE DETAILS

For further package information, please contact Customer Services. All dimensions in mm, unless stated otherwise. DO NOT SCALE.



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